ELECTRIC POWER STORAGE

Inventor: James F. Corum, Morgantown, WV (US)

Assignee: CPG Technologies, LLC, Newbury, OH (US)

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PRIMARY EXAMINER—Albert W Puladini
ATTORNEY, AGENT, OR FIRM—Thomas, Kayden, Horstemeyer & Risley, LLP

ABSTRACT

In various embodiments, various systems and methods are provided for power storage. In one embodiment, a power storage apparatus is described that comprises a power multiplier having a multiply-connected electrical structure. A parametric reactance is included in the multiply-connected electrical structure that negates at least a portion of a physical resistance of the multiply-connected electrical structure. A parametric excitation source having a parametric excitation output is applied to the parametric reactance.

20 Claims, 3 Drawing Sheets
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FIG. 1
FIG. 3
ELECTRIC POWER STORAGE

CROSS-REFERENCE TO RELATED APPLICATION

This application is a continuation-in-part of co-pending U.S. Utility patent application Ser. No. 11/670,620 entitled, "PARAMETRIC POWER MULTIPLICATION," filed on Feb. 2, 2007, which is incorporated herein by reference in its entirety.

BACKGROUND

Power multiplication may be desirable for many applications that require significant power resources that cannot be economically or physically provided given the current state of power technology. For example, some have attempted to use conventional mechanical flywheel and capacitive storage arrangements for energy storage and power multiplication. However, such approaches are often inadequate due to the decay in amplitude and/or frequency of power output as stored energy is extracted or released.

Power multiplication may also be achieved electrically using an electromagnetic path configuration for accumulating electrical energy and stepping up or magnifying real AC power. Such technology has been taught by Tischer, F. J., Resonance Properties of Ring Circuits, IEEE Transactions on Microwave Theory and Techniques, Vol. MTT-5, 1957, pp. 51-56. The power multiplier suggested by Tischer makes it possible to obtain practical power multiplication of 10 to 500 times the output power level of a given generator. The power multiplication is obtained without appreciable decay in either amplitude or frequency.

However, the power multiplier suggested by Tischer operates at relatively short wavelengths where the physical circumference of the device is on the order of an integer number of free space wavelengths given that the electrical length of the electromagnetic path suggested by Tischer equals an integer multiple of the wavelength of a traveling wave multiplied therein. At such short wavelengths, the physical size of the electromagnetic path is such that it can be practically constructed. However, power multiplication using an approach suggested by Tischer is not practical at lower power frequencies such as 60 Hertz with relatively long wavelengths as the size of the electromagnetic path would be on the order of several hundred miles. In addition, the maximum power that can be stored in the power multiplier suggested by Tischer is limited by the resistance of the waveguide.

In current electrical distribution systems such as the North American power grid it is often the case that Utilities experience severe mismatches between peak and average load demands. This can result in brown outs and blackouts in the system. Also, the North American power grid is being stretched to capacity. Consequently, it can be the case that brown outs and black outs may start chain reactions in the power grid that results in loss of reliable power.

In addition, another problem that energy markets face is that intervening load points such as cities often separate power generation stations from remote electrical loads. During heavy load times, the demand throughput cannot be conveyed from the power generation stations to the remote loads around the intermediate cities.

BRIEF DESCRIPTION OF THE DRAWINGS

Many aspects of the invention can be better understood with reference to the following drawings. The components in the drawings are not necessarily to scale, emphasis instead being placed upon clearly illustrating the principles of the present invention. Moreover, in the drawings, like reference numerals designate corresponding parts throughout the several views.

FIG. 1 is a schematic diagram of a power multiplier that employs parametric excitation to store electrical energy according to an embodiment of the present invention;

FIGS. 2A-2C are block diagrams of various examples of sources of a parametric excitation output that is applied to a parametric reactance in the power multiplier of FIG. 1 according to various embodiments of the present invention; and

FIG. 3 is a schematic diagram of a power multiplier that employs parametric excitation to store electrical energy obtained from a power grid according to an embodiment of the present invention.

DETAILED DESCRIPTION

The various embodiments of the present invention include the use of power multipliers as described in U.S. patent application Ser. No. 11/670,620 filed on Feb. 2, 2007, which is incorporated herein by reference in its entirety. The power multipliers as described herein may be constructed from lumped elements or distributed elements as set forth in the above described U.S. patent application. For purposes of the discussion herein, one embodiment of a power multiplier is described herein that is constructed from lumped elements. However, it is understood that in other embodiments of the present invention, power multipliers may be employed that are constructed of distributed elements, or a combination of both lumped and distributed elements.

Turning then, to FIG. 1, shown is an example of a power multiplier 100 according to an embodiment of the present invention. The power multiplier 100 includes a power multiplying network 103 and a launching network 106. The launching network 106 is coupled to the power multiplying network 103 via a directional coupler 109 that couples the launching network 106 to the power multiplying network 103. A power source 113 is coupled to the launching network 106. Also, the launching network 106 is terminated in a matching load R_L.

According to one embodiment, the power multiplying network 103 is a multiply-connected, velocity inhibiting circuit constructed from a number of lumped-elements. As contemplated herein, the term "network" refers to an interconnected structure of electrical elements. The terms "multiply-connected" are mathematical terms relating to the existence of a closed path in a resonator, waveguide, or other electrical structure that cannot be reduced to a point without part of the closed path passing through regions that are external to the geometrical boundaries of the resonator, waveguide, or other electrical pathway. The power multiplying network 103 is "velocity inhibiting" as the electrical structure of the power multiplying network 103 results in a reduced velocity of propagation of an electromagnetic wave through the power multiplying network 103 relative to the speed of an electromagnetic wave through free space, which is the speed of light.

In addition, the term "lumped" refers to elements that are effectively concentrated at a single location. Thus, the terms "lumped-elements" refer to discrete, two-terminal, concentrated electrical elements such as capacitance, inductance, resistance, and/or conductance. Thus, the lumped-elements as described herein may comprise discrete inductors, capacitors, or resistors. In addition, as contemplated herein, lumped-elements may also comprise diodes, transistors, and
other semi-conductors that may be described, for example, as nonlinear resistors or conductors that have resistance or conductance that is controlled by the polarity of applied voltages or currents, etc. In addition, lumped-elements may also comprise inherent capacitances, inductances, resistances, or conductances of various electrical structures such as helices, parallel plates, or other structure as will be discussed. Similar to the power multiplying network 103, the directional coupler 109 may also be constructed using lumped-elements. Thus, the power multiplying network 103 provides one example of a multiply-connected circuit that may comprise, for example, a circuit formed in a ring. Alternatively, according to other embodiments, a power multiplier may be constructed out of distributed element components formed in a multiply-connected electrical structure such as a ring. For example, an alternative multiply-connected electrical structure may comprise a loop of coaxial cable or other similar structure.

According to one embodiment, the power multiplying network 103 is a velocity inhibiting circuit that results in a slower velocity of propagation of an electrical disturbance such as a traveling wave. In this respect, the power multiplying network 103 has an electrical length that is equal to an integer multiple of the wavelength of the operating frequency of the power source 113. Due to the velocity inhibited nature of the power multiplying network 103, its size is quite compact in comparison with the wavelength of the operating frequency of the power source 113. In addition, the directional coupler 109 causes a phase shift that is equal to one quarter of the wavelength of an exciting traveling wave generated by the power source 113 at the operating frequency as will be discussed.

The power multiplier 100 also includes a phase shifter 119 as shown. The phase shifter 119 comprises, for example, a circuit constructed from lumped-elements that is combined in series with a portion of the directional coupler 109 to make up an inductance L(t) of the specific section within which the directional coupler 109 is located.

In one embodiment, the power multiplying network 103 is constructed from lumped-elements such as, for example, parametric reactances that include variable inductances L(t) and variable capacitances C(t). Common examples of time-varying or parametric reactances are inductors and capacitors whose permittivity and permeability functions are pumped in time by a control voltage or current. Similarly, distributed time-varying impedances have their constitutive parameters pumped by a control signal, which may be electrical, electromagnetic, optical, thermal, mechanical, acoustical, etc. For a more detailed discussion of the parametric reactances comprising the variable inductances L(t) and variable capacitances C(t), reference is made to the discussion in the co-pending U.S. patent application Ser. No. 11/670,620 entitled, “PARAMETRIC POWER MULTIPLICATION,” filed on Feb. 2, 2007 and incorporated by reference above.

The power multiplier 100 also includes a diverter 123 that is coupled to a load 426. The diverter 123 is configured to divert power from the power multiplying network 103 to a load 126 in a controlled manner. Specifically, all of the power may be diverted out of the power multiplying network 103 in a relatively short pulse that may last, for example, on the order of microseconds. Alternatively, the diverter 123 may be configured to divert a portion of the power stored in the power multiplying network 103. Stated another way, the power diverted to the load 126 may be less than a total power stored in the power multiplying network 103. To this end, it may be possible to supply power to the load 126 for a significant period of time. Such a time period would depend upon the amount of power stored in the power multiplying network 103 or ring as well as the magnitude of the power diverted to the load 126 and is application specific.

According to the various embodiments, one or more of the parametric reactances in the power multiplying network 103 are varied in time at a frequency that is in a predefined relationship relative to the operating frequency of the power source 113. That is to say, the frequency of at which the parametric reactances are varied in time is in a predefined relationship relative to the frequency of a traveling wave in the ring formed by the power multiplying network 103. For a more specific discussion as to the relationship between the frequency of the power source 113 and the frequency at which the parametric reactances are varied, reference is made once again to U.S. patent application Ser. No. 11/670,620 entitled, “PARAMETRIC POWER MULTIPLICATION,” filed on Feb. 2, 2007 and incorporated by reference above.

According to one embodiment, the parametric reactances L(t) and C(t) are varied based upon a parametric excitation output or signal 133. The parametric excitation signal/output may be generated in any one of a number of ways. In one embodiment, the parametric excitation output 133 is generated by a parametric excitation source 136. In one embodiment, the parametric excitation output 133 generated by the parametric excitation source 136 is applied to a phase corrector 139. The phase corrector 139 provides for the adjustment of the phase of the parametric excitation output 133 so as to align properly with the traveling wave in the power multiplying network 103, thereby resulting in parametric gain as was described in U.S. patent application Ser. No. 11/670,620 entitled, “PARAMETRIC POWER MULTIPLICATION,” filed on Feb. 2, 2007 and incorporated by reference above. The parametric excitation source 136 may comprise anyone of a plurality of different types of sources as will be described.

Referring next to FIG. 2A, shown is one example of a parametric excitation source 136a according to an embodiment of the present invention. The parametric excitation source 136a comprises a DC battery that generates a DC output that is coupled to an oscillator 146. The oscillator 146 converts the DC voltage to an AC voltage. The frequency of oscillation of the AC voltage generated by the oscillator 146 is specified so as to result in parametric excitation of the multiply-connected electrical structure as described above. In addition, for the parametric excitation source 136a, the oscillator 146 may actually act in place of the phase corrector 139 to the extent that the phase of the output of the oscillator 146 can be adjusted.

Referring next to FIG. 2B, shown is another example of a parametric excitation source 136b according to an embodiment of the present invention. The parametric excitation source 136b comprises an AC generator 149. The AC generator 149 may be configured to generate the parametric excitation signal at the required frequency for parametric excitation of the multiply-connected electrical structure as described above. Alternatively, the output of the AC generator 149 may be applied to a frequency converter as can be appreciated so as to result in the proper frequency applied to the parametric reactances of the power multiplier 100 for parametric excitation of the multiply-connected electrical structure.

Still further, with reference to FIG. 2c, shown is another example of a parametric excitation source 136c according to an embodiment of the present invention. The parametric excitation source 136c includes a power multiplier 153 that operates at the appropriate frequency necessary for parametric excitation of the multiply-connected electrical structure according to an embodiment of the present invention.

The use of various parametric excitation sources 136a, 136b, 136c, or other parametric excitation sources facilitates
the storage of large amounts of electrical energy in the multiply-connected electrical structure of the power multiplier 100 that can be diverted at relatively large power levels even though the parametric excitation source 136 may actually have a power rating that is much smaller in magnitude than the diverted output. Stated another way, the electrical energy that may be stored in the multiply-connected electrical structure can be much greater than the amount of electrical energy that can be supplied by the parametric excitation sources 136.

With reference back to FIG. 1, the power multiplier 100 may be employed as an AC storage device. In this sense, the power multiplier 100 configured as described above may act as an electrical flywheel or perhaps an AC battery. In this sense, the parametric excitation source 136 generates the parametric excitation output 133 that is applied to the various parametric reactances 141 in the multiply-connected electrical structure such as the power multiplying network 103. The parametric reactances 141 are driven by the parametric excitation output 133 at the appropriate frequency such as, for example, twice the frequency of the power source 113 that supplies power to the multiply-connected electrical structure. Alternatively, other frequencies may be employed.

As a result, a negative resistance is created in the multiply-connected electrical structure that negates at least a portion of a physical resistance of the multiply-connected electrical structure. For a more detailed discussion of the frequency of the parametric excitation output 133 and the creation of a negative resistance in the multiply-connected electrical structure, references is made once again to U.S. patent application Ser. No. 11/670,620 entitled, “PARAMETRIC POWER MULTIPLICATION,” filed on Feb. 2, 2007 and incorporated by reference above.

If the magnitude of the parametric excitation output 133 is great enough, then substantially the entire physical resistance of the multiply-connected electrical structure may be negated such that the multiply-connected electrical structure either approaches superconductivity or becomes superconductive. Thus, by applying the parametric excitation output 133 to the parametric reactances in the multiply-connected electrical structure, the physical resistance of the multiply connected electrical structure is reduced or eliminated. This reduces or eliminates the loss of electrical energy traveling through the multiply-connected electrical structure due to the physical resistance of the multiply-connected electrical structure. As a result, it may be possible to store massive amounts of electrical energy in the multiply-connected electrical structure with little loss. Once the magnitude of the parametric excitation output 133 is specified so as to substantially eliminate the effective resistance within the multiply-connected electrical structure, then the resulting superconductivity or near superconductivity allows for the buildup of massive amounts of electrical energy in the multiply-connected electrical structure that can be released over a relatively long period of time or within a short pulse depending upon the specific application.

Given that the resistance of the multiply-connected electrical structure can be reduced to zero or near zero, then it would be possible to build up massive amounts of electrical energy in the multiply-connected electrical structure. Once the electrical energy is built up to the desired level, it can then be maintained in the structure with little or no loss over long periods of time simply by applying an appropriate parametric excitation output 133 to the respective parametric reactances of the multiply-connected electrical structure. The amount of electrical energy that may be stored within a given multiply-connected electrical structure may depend upon the physical limits of the structure in that, as voltages increase, the possibility of arcing and other phenomena increase as well. Thus, the physical limitations of the structure may dictate the ultimate maximum energy storage capacity of a given multiply-connected electrical structure. As such, the ultimate capacity of the multiply-connected electrical structure is design specific.

Given the fact that a massive amount of energy may be stored in a multiply-connected electrical structure and maintained by the proper application of a parametric excitation output 133 to the respective parametric reactances in the multiply-connected electrical structure, the power multipliers provide significant possibilities for energy storage for use upon loss of a primary power source. In one embodiment, the parametric excitation source 136 may be isolated from a power source such as power source 113 (FIG. 1) that generates the electrical power that is applied to the multiply-connected electrical structure through the directional coupler 109. By virtue of the fact that the parametric excitation source 136 is isolated from the power source 113, the amount of energy built up or stored in multiply-connected electrical structure can be maintained upon a failure of the power source 113. Thus, according to various embodiments of the present invention, the power multiplier may include parametric excitation sources 136 that are isolated from the power source 113 so that power may be built up into the multiply-connected electrical structures and stored until needed.

With reference to FIG. 3, shown is a schematic of a power multiplier 100 employed as a power storage device according to an embodiment of the present invention. A power multiplier 100 includes the power multiplying network 103 with a directional coupler 109, the diverter 123, and the load 126. As described above, the power multiplying network 103 is a multiply-connected electrical structure. The directional coupler 109 is also coupled to the termination resistance R_0. However, the power source 113 is not shown, where such power source 113 is coupled to a distribution grid 163. The distribution grid 163 may comprise, for example, the 60 Hz North American power grid or other power grid. In addition, a control system 166 is coupled to the grid 163. In one embodiment, the control system 166 is configured to detect whether the power source 113 driving the grid 163 has failed resulting, for example, in a brown out or black out. Also, the control system 166 includes control outputs to the diverter 123, a first switching element 169, and a second switching element 173.

The first switching element 169 is employed to direct electrical power from the grid 163 to the directional coupler 109 so as to build up electrical energy in the multiply-connected electrical structure such as the power multiplying network 103. In this manner, the control system 166 is employed to control whether power is to be stored in the multiply-connected electrical structure of the power multiplier 100. According to one embodiment, the control system 166 may be configured or manipulated to apply power from the grid to the multiply-connected electrical structure of the power multiplier 100 at times of low demand on the grid 163 when the costs of electrical energy are low.

FIG. 3 further depicts a frequency converter 176 and a backup parametric excitation source 179 that both include power outputs coupled to the second switching element 173. The frequency converter 173 includes a power input coupled to the grid 163 to receive power from the grid 163. The frequency converter 176 is employed to convert the frequency a power signal received from the grid 163 into the frequency of the parametric excitation output 133. Also, the frequency converter 176 may be configured to control the magnitude of the voltage of the parametric excitation output 133 so as to
effectively negate the resistance of the multiply-connected electrical structure. The frequency converter 173 is present in this scenario since the frequency of the parametric excitation output 133 should be at least twice the frequency of the power from the grid 163 that is applied to the multiply-connected electrical structure of the power multiplier 100 as mentioned above.

The second switching element 173 directs the output of the frequency converter 176 to the phase corrector 139. After undergoing phase correction, the parametric excitation output 133 is applied to the parametric reactances of the multiply-connected electrical structure of the power multiplier 100. Upon detection of a loss of power on the grid 163, the control system 166 is configured to cause the switching element 173 to switch to the backup parametric excitation source 179. The backup parametric excitation source 179 may comprise any one of the parametric excitation sources 136 as described with reference to FIGS. 2a-2c, or any other parametric excitation sources that would be suitable for the intended purpose. The switching element 173 may comprise such a device as to minimize or prevent any substantial interruption of the parametric excitation output 133 applied to the parametric reactances of the multiply-connected electrical structure. For example, the switching element 173 may comprise a solid state switch or other component that operates with enough speed so as to switch over the backup parametric excitation source 179 with little interruption in the parametric excitation output 133 applied to the parametric reactances. This is because a momentary drop in the magnitude of the parametric excitation output 133 will create in a momentary “spike” of resistance in the multiply-connected electrical structure that will result in an unwanted loss of energy.

Among the many benefits of the various embodiments of the present invention include the fact that power may be built up into a multiply-connected electrical structure as described above at low load times on the electrical grid. Such low load times may occur, for example, at night time when power rates may be relatively inexpensive. The parametric excitation sources 136 may then be employed to maintain substantially all of the electrical energy in the multiply-connected electrical structures until peak load times when such energy might be needed to supplement existing generation capacity and to provide for power smoothing, etc.

It should be emphasized that the above-described embodiments of the present invention are merely possible examples of implementations, merely set forth for a clear understanding of the principles of the various embodiments of the present invention. Many variations and modifications may be made to the above-described embodiment(s) of the invention without departing substantially from the spirit and principles of the various embodiments of the invention. All such modifications and variations are intended to be included herein protected by the following claims.

Therefore, having thus described the invention, at least the following is claimed:

1. A power storage apparatus, comprising:
   a power multiplier having a multiply-connected electrical structure;
   a parametric reactance that negates at least a portion of a physical resistance of the multiply-connected electrical structure; and
   a parametric excitation source having a parametric excitation output applied to the parametric reactance.

2. The power storage apparatus of claim 1, wherein the parametric excitation output is of a magnitude that substantially negates the physical resistance of the multiply-connected electrical structure.

3. The power storage apparatus of claim 1, wherein the parametric excitation source is isolated from a power source that generates a source output that is applied to the multiply-connected electrical structure through a directional coupler, thereby allowing an amount of energy to be maintained in the multiply-connected electrical structure upon a failure of the power source.

4. The power storage apparatus of claim 1, wherein the parametric excitation source further comprises a DC battery and an oscillator.

5. The power storage apparatus of claim 1, wherein the parametric excitation source further comprises a generator.

6. The power storage apparatus of claim 1, wherein the parametric excitation source further comprises a second power multiplier.

7. The power storage apparatus of claim 1, wherein the parametric excitation source further comprises:
   a frequency converter having a utility input;
   a backup parametric excitation source; and
   a switching mechanism that alternatively applies an output of the frequency converter or an output of the backup parametric excitation source to the parametric reactance.

8. The power storage apparatus of claim 1, further comprising a diverter circuit that diverts power from the multiply-connected electrical structure to a load, wherein the power diverted to the load is less than a total power stored in the multiply-connected electrical structure.

9. A method for storing power comprising the steps of:
   storing an amount of electrical energy in a multiply-connected electrical structure of a power multiplier;
   producing a parametric excitation output; and
   applying the parametric excitation output to at least one parametric reactance in the multiply-connected electrical structure to negate at least a portion of a physical resistance of the multiply-connected electrical structure to reduce a loss of the electrical energy in the multiply-connected electrical structure due to the physical resistance.

10. The method of claim 9, further comprising the step of isolating a source of the parametric excitation output from an input power applied to the multiply-connected electrical structure, wherein the electrical energy is maintained in the multiply-connected electrical structure upon a loss of the input power.

11. The method of claim 9, further comprising the step of negating substantially all of the physical resistance of the multiply-connected electrical structure to minimize the loss of the electrical energy in the multiply-connected electrical structure.

12. The method of claim 9, wherein the step of producing the parametric excitation output further comprises the step of generating the parametric excitation output using a DC battery and an oscillator.

13. The method of claim 9, wherein the step of producing the parametric excitation output further comprises the step of generating the parametric excitation output using a generator.

14. The method of claim 9, wherein the step of producing the parametric excitation output further comprises the step of obtaining the parametric excitation output from power stored in a second power multiplier.

15. The method of claim 9, wherein the step of producing the parametric excitation output further comprises the steps of:
   converting a power input into the parametric excitation output using a frequency converter; and
generating the parametric excitation output using a backup parametric excitation source upon a loss of the power input.

16. The method of claim 15, further comprising the step of switching between the power input and the backup parametric excitation source upon a loss of the power input without a substantial interruption of the parametric excitation output applied to the at least one parametric reactance.

17. The method of claim 9, further comprising the step of diverting power stored in the multiply-connected electrical structure to a load, wherein the power diverted to the load is less than a total power stored in the multiply-connected electrical structure.

18. A system for storing power comprising:
   a power multiplier having a multiply-connected electrical structure to store an amount of electrical energy;
   means for producing a parametric excitation output; and
   at least one parametric reactance in the multiply-connected electrical structure, wherein the parametric excitation output is applied to the parametric reactance to negate at least a portion of a physical resistance of the multiply-connected electrical structure to reduce a loss of the electrical energy in the multiply-connected electrical structure due to the physical resistance.

19. The system of claim 18, wherein means for producing the parametric excitation output further comprises a DC battery and an oscillator.

20. The system of claim 18, wherein means for producing the parametric excitation output further comprises a generator.

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